

U.S. Department of Commerce, Patent and Trademark Office

Application No.: 10/678,336

Filing date: October 3, 2003

**2nd SUPPLEMENTAL INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Inventors: Laurence B. Boucher, et al.

Group Art Unit: Unknown

**METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING
WITH A HIGH PERFORMANCE NETWORK INTERFACE**

Examiner name: Unknown

Attorney Docket No. ALA-008H

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
/M.D./	A	2003/ 0066011	4/3/03	Oren			
	B	2001/ 0048681	12/6/01	Bilic et al.			
	C	2001/ 0053148	12/20/01	Bilic et al.			
	D	2001/ 0014954	8/16/01	Purcell et al.			
	E	2001/ 0014892	8/16/01	Gaither et al.			
	F	2001/ 0013059	8/9/01	Dawson et al.			
	G	4,589,063	5/13/86	Shah et al.			
	H	5,778,013	7/7/98	Jedwab			
	I	6,038,562	3/14/00	Anjur et al.			
	J	6,070,200	5/30/00	Gates et al.			
	K	6,101,555	8/8/00	Goshey et al.			
	L	6,145,017	11/7/00	Ghaffari			
	M	6,157,955	12/5/00	Narad et al.			
	N	6,172,980	1/9/01	Flanders et al.			
	O	6,202,105	3/13/01	Gates et al.			
	P	6,279,051	8/21/01	Gates et al.			
	Q	6,298,403	10/2/01	Suri et al.			
	R	6,334,153	12/25/01	Boucher et al.			
	S	6,389,479	5/14/02	Boucher et al.			
	T	6,421,742	7/16/02	Tillier			
	U	6,480,489	11/12/02	Muller et al.			
	V	6,526,446	2/25/03	Yang et al.			
	W						
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Examiner

/Mon Cheri Davenport/

Date Considered

11/25/2007

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

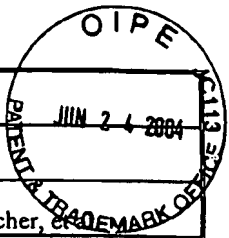


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U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
/M.D./	A	5,598,410	1/28/1997	Stone	370	469	
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	C						
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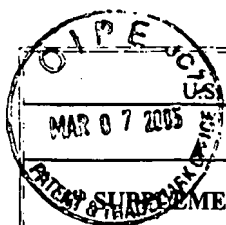
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4th SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Laurence B. Boucher, et al.
	Group Art Unit: Unknown
METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE	Examiner name: Unknown
	Attorney Docket No. ALA-008H

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
/M.D./	A	5,517,668	5/14/1996	Szwerinski et al.	395	800	
	B						
	C						
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/M.D./	A	5,524,250	6/4/96	Chesson et al.	395	775	
	B	5,619,650	4/8/97	Bach et al.	395	200.01	
	C	5,727,142	3/10/98	Chen	395	181	
	D	5,802,258	9/1/98	Chen	395	182.08	
	E	5,898,713	4/27/99	Melzer et al.	371	53	
	F	6,021,507	2/1/00	Chen	714	2	
	G	6,047,323	4/4/00	Krause	709	227	
	H						

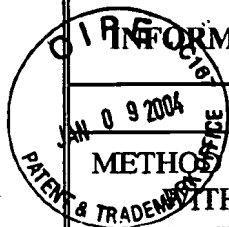
OTHER ART—NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
/M.D./	1	Schwaderer et al., IEEE Computer Society Press publication entitled, "XTP in VLSI Protocol Decomposition for ASIC Implementation", from 15 th Conference on Local Computer Networks, 5 pages, Sept. 30 – Oct. 3, 1990.
	2	Beach, Bob, IEEE Computer Society Press publication entitled, "UltraNet: An Architecture for Gigabit Networking", from 15 th Conference on Local Computer Networks, 18 pages, Sept. 30 – Oct. 3, 1990.
	3	Chesson et al., IEEE Symposium Record entitled, "The Protocol Engine Chipset", from Hot Chips III, 16 pages, Aug. 26-27, 1991.
	4	Macleane et al., IEEE Global Telecommunications Conference, Globecom '91, presentation entitled, "An Outboard Processor for High Performance Implementation of Transport Layer Protocols", 7 pages, Dec. 2-5, 1991.
	5	Ross et al., IEEE article entitled "FX1000: A high performance single chip Gigabit Ethernet NIC", from Compcon '97 Proceedings, 7 pages, Feb. 23-26, 1997.
	6	Strayer et al., "Ch. 9: The Protocol Engine" from XTP: The Transfer Protocol, 12 pages, July 1992.
	7	Publication entitled "Protocol Engine Handbook", 44 pages, Oct. 1990.
	8	Koufopavlou et al., IEEE Global Telecommunications Conference, Globecom '92, presentation entitled, "Parallel TCP for High Performance Communication Subsystems", 7 pages, Dec. 6-9, 1992.
	9	Lillenkamp et al., Publication entitled "Proposed Host-Front End Protocol", 56 pages, Dec. 1984.

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		Group Art Unit: Unknown
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		Attorney Docket No. ALA-008H

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
/M.D./	A	4,336,538	12/28/92	Johnson et al.	364	200	
	B	5,097,442	03/17/92	Ward et al.	365	78	
	C	5,629,933	05/13/97	Delp et al.	370	411	
	D	5,664,114	09/02/97	Krech, Jr. et al.	395	200.64	
	E	5,794,061	08/11/98	Hansen et al.	395	800.01	
	F	5,802,580	09/01/98	McAlpice	711	149	
	G	5,815,646	09/29/98	Purcell et al.	395	163	
	H	5,771,349	06/23/98	Picazo, Jr. et al.	395	188.01	
	I	6,009,478	12/28/99	Panner, et al.	710	5	
	J	5,056,058	10/8/91	Hirata, et al.	364	900	
	K	5,913,028	6/15/99	Wang, et al.	395	200.33	
	L						
	M						

Foreign Patent Documents								
							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
/M.D./	N	WO 99/65219 (PCT/US/99/13184)	6/10/99	International				
↓	O	WO 01/05107 A1 (PCT/US00/19006)	7/13/00	International				
	P	WO 01/05116A2 (PCT/US00/19243)	7/13/00	International				
	Q	WO 01/05123 A1 (PCT/US00/18976)	7/12/00	International				
	R	WO 01/04770A2 (PCT/US00/18939)	7/11/00	International				
	S	(WO 00/13091) PCT/US98/24943	11/20/98	International				

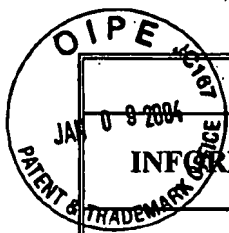
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
/M.D./	A	60/053,240 (U.S. Provisional Application), by Jolitz et al. (listed filing date July 18, 1997).
	B	Zilog Product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages (1997).
	C	Internet web pages from "Adaptec.com" website directed to the Adaptec, AEA-7110C iSCSI Host Bus Adapter and about Adaptec's IP storage activities, 11 pages, downloaded and printed 10/1/01.
	D	Internet web pages from "iSCSIhba.com" website directed to JNI iSCSI HBAs including the "FCE-3210/6410", 6 pages, downloaded and printed 10/1/01.
	E	Internet web pages from the "iSCSI Storage.com" website that mention an Emulex HBA, 2 pages, downloaded and printed 10/1/01.
	F	Internet web pages from the "iSCSIhba.com" website that mentions QLogic HBAs including the "SANblade 2300 Series", 8 pages, downloaded and printed 10/1/01.
	G	Internet pages from iReady News Archives entitled "iReady Rounding Out management team with two key executives," 2 pages, (printed 11/28/98).
	H	Toshiba, "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady's Design", 3 pages, Press release 10/14/98, downloaded 11/28/98.
	I	Internet pages entitled "iReady Products" from website http://www.ireadyco.com/products.html , 2 pages, printed 11/25/98.
	J	iReady News Archives. Toshiba, iReady shipping Internet Chip, 1 page, printed 11/28/98.
	K	Internet site www.interprophet.com , 17 pages, printed 3/1/00.
	L	The I-1000 Internet Tuner Features, iReady Corporation, 2 pages, date unknown.
	M	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
	N	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, 15 pages, September 1996.
	O	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
	P	VT8501 Apollo MVP4 Documentation, VIA Technologies, Inc., pages i-iv, 1-11, cover and copyright page, 21 pages, revision 1.3 (Feb. 1, 2000).
	Q	Internet pages entitled: Technical White Paper-Xpoint's Disk to LAN Acceleration Solution for Windows NT Server, 14 pages, printed 6/5/97.
	R	Jato Technologies Internet pages entitled: Network Accelerator Chip Architecture, twelve-slide presentation, 13 pages, printed 8/19/98.
	S	EETIMES article entitled: Enterprise System Uses Flexible spec, dated August 10, 1998, 3 pages, printed 11/25/98.
	T	Internet pages entitled: Smart Ethernet Network Interface Cards which Berend Ozceri is developing, 2 pages, printed 11/25/98.
	U	Internet pages of Xaqti corporation entitled: Giga Power Protocol Processor Product review, 4 pages, printed 11/25/99.
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
/M.D./	A	Internet pages from website http://www.ireadyco.com/about.html , 3 pages, downloaded 11/2/98.
	B	IReady News Archives, "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", San Jose, CA. 2 pages, 11/20/97, downloaded and printed 11/02/98.
	C	IReady News Archives, "Seiko Instruments Inc. (SII) Introduces World's First Internet-Ready Intelligent LCD Modules Based on IReady Technology", 2 pages 10/26/98, downloaded and printed 11/2/98.
	D	NEWSwatch - IReady Internet Tuner to Web Enable Devices, November 5, 1996. 2 pages, printed 11/02/98.
	E	David Lammers, EETimes, 06/13/97, "Tuner for Toshiba, Toshiba taps iReady for Internet tuner," 2 pages, printed 11/02/98.
	F	Internet pages entitled: Comparison of Novell Netware and TCP/IP Protocol Architectures, by Janique S. Carbone, 19 pages, July 16, 1995, downloaded and printed 4/10/98.
	G	Internet pages entitled : Hardware Assisted Protocol Processing, which Eugene Feinberg is working on, 1 page, printed 11/25/98.
	H	Internet pages of Xpoint Technologies www.xpoint.com web site, 5 pages, printed 12/19/97.
	I	Internet pages entitled: Asante and 100BASE-T Fast Ethernet, 7 pages, printed 5/27/97.
	J	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University, 13 pages, printed 12/21/98.
	K	Internet pages directed to: Technical Brief on Alteon Ethernet Gigabit NIC Technology, www.alteon.com , 14 pages, printed 3/15/97.
	L	Internet pages entitled: DART: Fast Application level Networking via Data-copy Avoidance, by Robert J. Walsh, 25 pages, printed 6/3/99.
	M	Internet pages of InterProphet entitled: Frequently Asked Questions, by Lynne Jolitz, 4 pages, printed 6/14/99.
	N	Internet pages entitled iREADY About Us and iREADY Products, 7 pages, printed 11/25/98.
↓	O	Andrew S. Tanenbaum, Computer Networks, Third Edition, 1996, ISBN 0-13-349945-6.
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U.S. Patent Documents

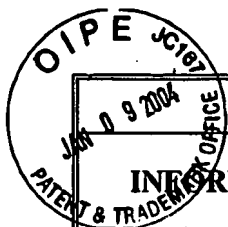
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/M.D./ 	A	4,991,133	2/5/91	Davis et al.	364	900
	B	5,163,131	11/10/92	Row et al.	395	200
	C	5,212,778	5/18/93	Dally et al.	395	400
	D	5,280,477	1/18/94	Trapp	370	85.1
	E	5,289,580	2/22/94	Latif et al.	395	275
	F	5,303,344	4/12/94	Yokoyama et al.	395	200
	G	5,412,782	5/2/95	Hausman et al.	395	250
	H	5,448,566	9/5/95	Richter et al.	370	94.1
	I	5,485,579	1/16/96	Hitz et al.	395	200.12
	J	5,506,966	4/9/96	Ban	395	250
	K	5,511,169	4/23/96	Suda	395	280

Foreign Patent Documents

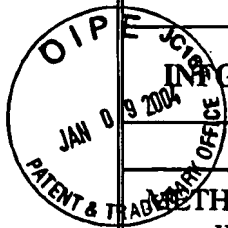
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↓	M	WO/98/50852 PCT/US98/0871	11/1298	International				
	N	WO/99/04343 PCT/US98/14729	7/17/98	International				
	O							

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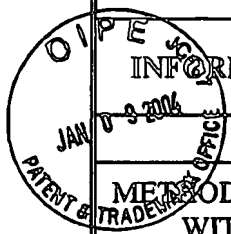
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/M.D./ ↓ V	A	5,548,730	8/20/96	Young et al.	395	280		
	B	5,558,121	12/24/96	Reddin et al.	395	200.15		
	C	5,590,328	12/31/96	Seno et al.	395	675		
	D	5,592,622	1/7/97	Isfel et al.	395	200.02		
	E	5,634,099	5/27/97	Andrews et al.	395	200.07		
	F	5,634,127	5/27/97	Cloud et a.	395	680		
	G	5,642,482	6/24/97	Pardillos	395	200.2		
	H	5,671,355	9/23/97	Collins	395	200.2		
	I	5,692,130	11/25/97	Shobu et al.	395	200.12		
	J	5,699,317	12/16/97	Sartore et al.	395	230.06		
	K	5,701,434	12/23/97	Nakagawa	395	484		
Foreign Patent Documents								
		Document Number	Date	Country	Class	Subclass	Translation	
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	N							
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<div style="font-size: 2em;">↓</div>	A	5,749,095	5/5/98	Hagersten	711	141	
	B	5,751,715	5/12/98	Chan et al.	370	455	
	C	5,752,078	5/12/98	Delp et al.	395	827	
	D	5,758,084	5/26/98	Silverstein et al.	395	200.58	
	E	5,758,089	5/26/98	Gentry et al.	395	200.64	
	F	5,758,186	5/26/98	Hamilton et al.	395	831	
	G	5,758,194	5/26/98	Kuzma	395	886	
	H	5,790,804	8/4/98	Osborne	395	200.75	
	I	5,812,775	9/22/98	Van Seters et al.	395	200.43	
	J	5,566,170	10/15/96	Bakke et al.	370	60	
	K	5,878,225	3/2/99	Bilansky et al.	395	200.57	
	L	5,930,830	7/27/99	Mendelson et al.	711	171	
	M	5,991,299	11/23/99	Radogna et al.	370	392	
	N	6,061,368	5/9/00	Hitzelberger	370	537	
	O	6,034,963	3/7/00	Minami et al.	370	401	
	P	5,678,060	10/14/97	Yokoyama et al.	395	831	
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/M.D./	A	4,366,538	12/28/82	Johnson et al.	364	200	10/31/80
	B	5,588,121	12/24/96	Reddin et al.	395	200.15	05/09/96
	C	5,701,516	12/23/97	Cheng et al.	395	842	01/19/96
	D	5,809,328	09/15/98	Nogales et al.	395	825	12/21/95
	E	5,931,918	08/03/99	Row et al.	709	300	07/30/97
	F	5,935,205	08/10/99	Murayama et al.	709	216	06/21/96
	G	5,937,169	08/10/99	Connery et al.	395	200.8	10/29/97
	H	5,941,969	08/24/99	Ram et al.	710	128	10/22/97
	I	5,941,972	08/24/99	Hoese et al.	710	129	12/31/97
	J	5,950,203	09/07/99	Stakuis et al.	707	10	12/31/97
	K	6,005,849	12/21/99	Roach et al.	370	276	09/24/97
	L	6,016,513	01/18/00	Glen H. Lowe	709	250	02/19/98
	M	6,026,452	02/15/00	William Michael Pitts	710	56	08/31/98
	N	6,044,438	03/28/00	Howard Thomas Olnowich	711	130	07/10/97
	O	6,047,356	04/04/00	Anderson et al.	711	129	04/18/94
	P	6,057,863	05/02/00	Sompong P. Olarig	345	520	10/31/97
	Q	6,065,096	05/16/00	Day et al.	711	114	09/30/97
	R	6,141,705	10/31/00	Anand et al.	710	15	06/12/98
	S	6,246,683	06/12/01	Connery et al.	370	392	05/01/98
	T	6,226,680	05/01/01	Boucher et al.	709	230	04/27/98
	U	6,247,060	06/12/01	Boucher et al.	709	238	11/12/99
	V	08/964,304		Napolitano et al.			11/04/97

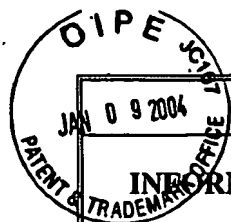
Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
/M.D./	W	WO 01/40960 A1	07/06/01	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner /Mon Cheri Davenport/ Date Considered 11/25/2007

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.



U.S. Department of Commerce, Patent and Trademark Office

Application No.: 10/678,336

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Filing date: October 3, 2003

Inventors: Laurence B. Boucher, et al.

Group Art Unit: Unknown

METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING
WITH A HIGH PERFORMANCE NETWORK INTERFACE

Examiner name: Unknown

Attorney Docket No. ALA-008H

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

/M.D./	A	"Two-Way TCP Traffic over Rate Controlled Channels: Effects and Analysis," by Lampros Kalampoukas, Anujan Varma and K.K. Ramakrishnan, IEEE Transactions on Networking, Volume 6, Number 6, 17 pages, December 1998.
	B	"Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design," iReady News Archives, 3 pages, downloaded November 2, 1998.
	C	"File System Design for an NFS File Server Appliance," by Dave Hitz, James Lau and Michael Malcolm, 12 pages, Copyright 1996.
	D	Adaptec press release, "Adaptec Announces EtherStorage Technology", 2 pages, June 14, 2000.
	E	Adaptec article entitled, "EtherStorage Frequently Asked Questions," 5 pages, July 19, 2000.
	F	Adaptec article entitled, "EtherStorage White Paper," 7 pages, July 19, 2000.
	G	Article entitled, "Computers; Storage," by James Berlino and Ananda Baruah, CIBC World Markets Equity Research, 9 pages, August 7, 2000.
	H	Merrill Lynch article entitled, "Storage Futures," by Steven Milunovich, 22 pages, May 10, 2000.
	I	Market Watch - News Articles, "Montreal start-up battles data storage bottleneck," by Susan Taylor, 2 pages, March 5, 2000.
↓	J	Internet-Draft "SCSI/TCP (SCSI over TCP)," by J.Satran et al., 38 pages, February 2000.
	K	
	L	
	M	
	N	

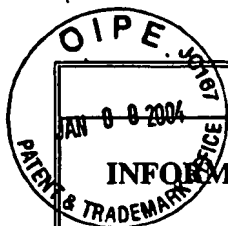
Examiner

/Mon Cheri Davenport/

Date Considered

11/25/2007

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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/678,336
INFORMATION DISCLOSURE STATEMENT BY APPLICANT METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE	Filing date: October 3, 2003
	Inventors: Laurence B. Boucher, et al.
	Group Art Unit: Unknown
	Examiner name: Unknown
	Attorney Docket No. ALA-008H

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
/M.D./	A	6,345,301	02/05/02	Burns et al.	709	230	
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						

Foreign Patent Documents

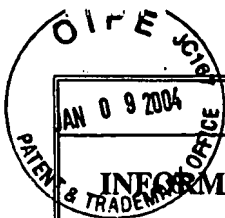
							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	K							
	L							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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	N	
	O	
	P	

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U.S. Patent Documents

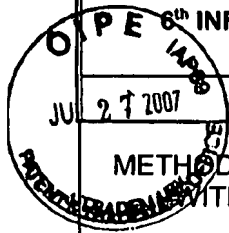
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
/M.D./	A	5,996,024	11/30/99	Blumenau	709	301	
	B	6,173,333	07/18/97	Jolitz et al.			
	C	2001/0025315A1		Jolitz			01/10/01
	D	2001/0004354A1		Jolitz			01/10/01
	E	5,058,110	10/15/91	Beach et al.	370	85.6	
	F	6,021,446	02/01/00	Gentry et al.	709	303	
	G	6,356,951	03/12/02	Gentry et al.	709	250	
	H	6,389,468	05/14/02	Muller et al.	709	226	
	I	6,434,651	08/13/02	Gentry, Jr.	710	260	
	J	6,427,169	07/30/02	Elzur	709	224	
	K	6,449,656	09/10/02	Elzur et al.	709	236	
	L	6,453,360	09/17/02	Muller et al.	709	250	

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/M.D./	M	Article entitled "LRP: A New Network Subsystem Architecture for Server Systems", by P. Druschel and G. Banga, Rice University, October 1996, 15 pages.
/M.D./	N	Internet RFC/STD/FYI/BCP Archives article with heading "RFC2140" entitled "TCP Control Block Interdependence", web address http://www.faqs.org/rfcs/rfc2140.html , 9 pages, printed 9/20/02.
	O	
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Examiner /Mon Cheri Davenport/	Date Considered 11/25/2007
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6 th INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: October 3, 2003
	Inventors: Laurence B. Boucher, et al.
	Group Art Unit: 2127
METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE	Examiner name: Unknown
	Attorney Docket No.: ALA-008H

Page 1 of 14

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Examiner Initial		Document	Date	Name	Class	Subclass	Filing Date
/M.D./	1	4,366,538	December 28, 1982	Johnson et al.	364	200	
	2	4,485,455	November 27, 1984	Gary W. Boone et al	364	900	
	3	4,485,460	November 27, 1984	Mark A. Stambaugh	365	203	
	4	4,700,185	October 13, 1987	Thomas J. Balph et al	340	825.5	
	5	4,991,133	May 13, 1986	Davis et al.	364	900	
	6	5,056,058	October 8, 1991	Hirata et al.	709	230	
	7	5,058,110	October 15, 1991	Beach et al.	370	85.6	
	8	5,097,442	March 17, 1992	Ward et al.	365	78	
	9	5,163,131	November 10, 1992	Row et al.	395	200	
	10	5,212,778	May 18, 1993	Dally et al.	395	400	
	11	5,280,477	January 18, 1994	Trapp	370	85.1	
	12	5,289,580	February 22, 1994	Latif et al.	395	275	
	13	5,303,344	April 12, 1994	Yokoyama et al.	395	200	
	14	5,412,782	May 2, 1995	Hausman et al.	395	250	
	15	5,418,912	May 23, 1995	David A. Christenson	395	200	
✓	16	5,448,566	September 5, 1995	Richter et al.	370	94.1	

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METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE	Examiner name: Unknown
	Attorney Docket No.: ALA-008H

Page 2 of 14

/M.D./	17	5,485,579	January 16, 1996	Hitz et al.	395	200.12	
	18	5,506,966	April 9, 1996	Ban	395	250	
	19	5,511,169	April 23, 1996	Suda	395	280	
	20	5,535,375	July 9, 1996	Eshel et al.	391	500	
	21	5,548,730	August 20, 1996	Young et al.	395	280	
	22	5,566,170	October 15, 1996	Bakke et al.	370	60	
	23	5,574,919	November 12, 1996	Arun N. Netravali et al	395	561	
	24	5,566,170	October 15, 1996	Bakke et al.	370	60	
	25	5,588,121	December 24, 1996	Reddin et al.	395	200.15	
	26	5,590,328	December 31, 1996	Seno et al.	395	675	
	27	5,592,622	January 7, 1997	Isfeld et al.	395	200.02	
	28	5,629,933	May 13, 1997	Delp et al.	370	411	
	29	5,633,780	May 27, 1997	Cronin et al.	361	220	
	30	5,634,099	May 27, 1997	Andrews et al.	395	200.07	
	31	5,634,127	May 27, 1997	Cloud et al.	395	680	
	32	5,642,482	June 24, 1997	Pardillos	395	200.2	
	33	5,664,114	September 2, 1997	Krech, Jr. et al.	395	200.64	
↓	34	5,671,355	September 23, 1997	Collins	395	200.2	

Examiner /Mon Cheri Davenport/	Date Considered 11/25/2007
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METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE	Examiner name: Unknown
	Attorney Docket No.: ALA-008H

Page 3 of 14

/M.D./	35	5,678,060	October 14, 1997	Yokoyama et al.	709	212	
	36	5,682,534	October 28, 1997	Kapoor et al.	395	684	
	37	5,692,130	November 25, 1997	Shobu et al.	395	200.12	
	38	5,699,317	December 16, 1997	Sartore et al.	395	230.06	
	39	5,699,350	December 16, 1997	Andrew J. Kraslavsky	370	254	
	40	5,701,434	December 23, 1997	Nakagawa	395	484	
	41	5,701,516	December 23, 1997	Cheng et al.	395	842	
	42	5,742,765	April 21, 1998	Wong et al.	395	200	
	43	5,749,095	May 5, 1998	Hagersten	711	141	
	44	5,751,715	May 12, 1998	Chan et al.	370	455	
	45	5,752,078	May 12, 1998	Delp et al.	395	827	
	46	5,758,084	May 26, 2008	Silverstein et al.	395	200.58	
	47	5,758,089	May 26, 2008	Gentry et al.	395	200.64	
	48	5,758,186	May 26, 2008	Hamilton et al.	395	831	
	49	5,758,194	May 26, 2008	Kuzma	395	886	
	50	5,768,618	June 16, 1998	Gene R. Erickson et al	395	829	
	51	5,771,349	June 23, 1998	Picazo, Jr. et al.	395	188.01	
✓	52	5,778,419	July 7, 1998	Hansen et al.	711	112	

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	Group Art Unit: 2127
METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE	Examiner name: Unknown
	Attorney Docket No.: ALA-008H

Page 4 of 14

/M.D./	53	5,790,804	August 4, 1998	Osborne	709	245	
	54	5,794,061	August 11, 1998	Hansen et al.	395	800.01	
	55	5,802,580	September 1, 1998	McAlpice	711	149	
	56	5,809,328	September 15, 1998	Nogales et al.	395	825	
	57	5,812,775	September 22, 1998	Van Seeters et al.	395	200.43	
	58	5,815,646	September 29, 1998	Purcell et al.	395	163	
	59	5,828,835	October 27, 1998	Mark S. Isfeld et al	395	200.3	
	60	5,848,293	December 8, 1998	Gentry et al.	395	825	
	61	5,872,919	February 16, 1999	Wakeland et al.	395	200	
	62	5,878,225	March 2, 1999	Bilansky et al.	395	200.57	
	63	5,892,903	April 6, 1999	Christopher W. Klaus	395	187.01	
	64	5,913,028	June 15, 1999	Wang, et al.	395	200.33	
	65	5,920,566	July 6, 1999	Ariel Hendel et al	370	401	
	66	5,930,830	July 27, 1999	Mendelson et al.	711	171	
	67	5,931,918	August 3, 1999	Row et al.	709	300	
	68	5,935,205	August 10, 1999	Murayama et al.	709	216	
	69	5,937,169	August 10, 1999	Connery et al.	395	200.8	
↓	70	5,941,969	August 24, 1999	Ram et al.	710	128	

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METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE	Examiner name: Unknown
	Attorney Docket No.: ALA-008H

Page 5 of 14

/M.D./	71	5,941,972	August 24, 1999	Hoese et al.	710	129	
	72	5,950,203	September 7, 1999	Stakuis et al.	707	10	
	73	5,970,804	August 4, 1998	Osborne	395	200.75	
	74	5,987,022	November 16, 1999	Robert L. Geiger et al	370	349	
	75	5,991,299	November 23, 1999	Radogna et al.	370	392	
	76	5,996,013	November 30, 1999	Gary Scott Delp et al	709	226	
	77	5,996,024	November 30, 1999	Blumenau	709	301	
	78	6,005,849	December 21, 1999	Roach et al.	370	276	
	79	6,009,478	December 28, 1999	Panner, et al.	710	5	
	80	6,016,513	January 18, 2000	Glen H. Lowe	709	250	
	81	6,021,446	February 1, 2000	Gentry et al.	709	303	
	82	6,026,452	February 15, 2000	William Michael Pitts	710	56	
	83	6,034,963	March 7, 2000	Minami et al.	370	401	
	84	6,041,058	March 21, 2000	John A. Flanders et al	370	401	
	85	6,041,381	March 21, 2000	Geoffrey B. Hoese	710	129	
	86	6,044,438	March 28, 2000	Howard Thomas Olnowich	711	130	
	87	6,047,356	April 4, 2000	Anderson et al.	711	129	
↓	88	6,049,528	April 11, 2000	Ariel Hendel et al	370	235	

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METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE	Examiner name: Unknown
	Attorney Docket No.: ALA-008H

Page 6 of 14

/M.D./	89	6,057,863	May 2, 2000	Sompong P. Olarig	345	520	
	90	6,061,368	May 9, 2000	Hitzelberger	370	537	
	91	6,065,096	May 16, 2000	Day et al.	711	114	
	92	6,067,569	May 23, 2000	Mohamed J. Khaki et al	709	224	
	93	6,078,733	June 20, 2000	Randy B. Osborne	395	200.8	
	94	6,097,734	August 1, 2000	Joel Gotesman et al	370	474	
	95	6,111,673	August 29, 2000	Gee-Kung Chang et al	359	123	
	96	6,115,615	September 5, 2000	Takeshi Ota et al	455	553	
	97	6,122,670	September 19, 2000	Toby D. Bennett et al	709	236	
	98	6,141,701	October 31, 2000	Mark M. Whitney	710	5	
	99	6,141,705	October 31, 2000	Anand et al.	710	15	
	100	6,173,333	July 18, 1997	Jolitz et al.			
	101	6,181,705	February 24, 2004	San-Hong Kim	370	392	
	102	6,223,242	April 24, 2001	Stephen J. Sheafor et al	710	132	
	103	6,226,680	May 1, 2001	Boucher et al.	709	230	
	104	6,246,683	June 12, 2001	Connery et al.	370	392	
	105	6,247,060	June 12, 2001	Boucher et al.	709	238	
↓	106	6,289,023	September 11, 2001	Brian M. Dowling et al	370	419	

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	Group Art Unit: 2127
METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE	Examiner name: Unknown
	Attorney Docket No.: ALA-008H

Page 7 of 14

/M.D./	107	6,324,649	November 27, 2001	Kevin W. Eyres et al	713	202	
	108	6,343,360	January 29, 2002	David Feinleib	713	1	
	109	6,345,302	February 5, 2002	Toby D. Bennett et	709	236	
	110	6,356,951	March 12, 2002	Gentry et al.	709	250	
	111	6,370,599	April 9, 2002	Sanjay Anand et al	710	15	
	112	6,385,647	May 7, 2002	Dean Willis et al	709	217	
	113	6,389,468	May 14, 2002	Muller et al.	709	226	
	114	6,421,753	July 16, 2002	Geoffrey B. Hoese et al	710	129	
	115	6,427,169	July 30, 2002	Elzur	709	224	
	116	6,427,173	July 30, 2002	Laurence B. Boucher et al	709	238	
	117	6,434,651	August 13, 2002	Gentry, Jr.	710	260	
	118	6,449,656	September 10, 2002	Elzur et al.	709	236	
	119	6,453,360	September 17, 2002	Muller et al.	709	250	
	120	6,473,425	October 29, 2002	Gilles Bellaton et al	370	392	
	121	6,487,202	November 26, 2002	Daniel E. Klausmeier et al	370	395	
	122	6,487,654	November 26, 2002	Eric M. Dowling	712	244	
	123	6,490,631	December 3, 2002	Paul R. Teich et al	709	250	
↓	124	6,502,144	December 31, 2002	Jean-Paul Accarie	710	8	

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	Group Art Unit: 2127
METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE	Examiner name: Unknown
	Attorney Docket No.: ALA-008H

Page 8 of 14

/M.D./	125	6,523,119	February 18, 2003	Dominique Vincent Pavlin et al	713	192	
	126	6,570,884	May 27, 2003	Glenn William Connery et al	370	419	
	127	6,591,310	July 8, 2003	Stephen B. Johnson	710	3	
	128	6,648,611	November 18, 2003	David M. Morse et al	417	310	
	129	6,650,640	November 18, 2003	Shimon Muller et al	370	392	
	130	6,657,757	December 2, 2003	Gee-Kung Chang et al	359	124	
	131	6,658,480	December 2, 2003	Laurence B. Boucher et al	709	239	
	132	6,678,283	January 13, 2004	Yakov Teplitsky	370	463	
	133	6,681,364	January 20, 2004	Jean Louis Calvignac et al	714	776	
	134	6,697,868	February 24, 2004	Peter K. Craft et al	709	230	
	135	6,765,901	July 20, 2004	Michael Ward Johnson et al	370	352	
	136	6,807,581	October 19, 2004	Daryl D. Starr et al	709	250	
	137	6,842,896	January 11, 2005	Mark E. Redding et al	717	172	
	138	6,912,522	June 28, 2005	David A. Edgar	707	2	
	139	6,941,386	September 6, 2005	Peter K. Craft et al	709	250	
↓	140	6,965,941	November 15, 2005	Laurence B. Boucher et al	709	230	

Published Applications

Examiner /Mon Cheri Davenport/	Date Considered 11/25/2007
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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/678,336
6 th INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: October 3, 2003
	Inventors: Laurence B. Boucher, et al.
	Group Art Unit: 2127
METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE	Examiner name: Unknown
	Attorney Docket No.: ALA-008H

Page 9 of 14

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/M.D./	141	2001/0004354	January 10, 2001	Jolitz	370	328	
	142	2001/0025315	January 10, 2001	Jolitz	709	231	
	143	2002/0073223	June 13, 2002	B. Scott Darnell et al	709	232	
	144	2002/0112175	August 15, 2002	Makofka et al	713	200	
	145	2003/0066011	April 11, 2002	Oren	714	758	
	146	2003/0110344	June 1, 2003	Szezepanek et al.	711	100	
	147	2004/0054814	March 1, 2004	McDaniel			
	148	2004/0059926	March 25, 2004	Angelo, et al.	713	168	
	149	2004/0153578	August 1, 2004	Elzur			
	150	2004/0213290	October 1, 2004	Johnson et al.	370	469	
↓	151	2004/0246974	December 9, 2004	Gyugyi et al.	370	395.31	

Foreign Patent Documents							
Examiner Initial		Document Number	Date	Country	Class	Subclass	Translation
/M.D./	152	WO 98/19412	May 7, 1998	PCT/US97/17257			
↓	153	WO 98/50852	November 12, 1998	PCT/US98/0871			
↓	154	WO 99/04343	January 28, 1999	PCT/US98/14729			

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Page 10 of 14

/M.D./	155	WO 99/65219	December 16, 1999	PCT/US/99/13184				
	156	WO 00/13091	March 9, 2000	PCT/US98/24943				
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/M.D./	164	Internet pages entitled "Hardware Assisted Protocol Processing", (which Eugene Feinber is working on), 1 page, printed 11/25/98.						
	165	Zilog product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages, 1997.						
	166	Internet pages of Xpoint Technologies, Inc. entitled "Smart LAN Work Requests", 5 pages, printed 12/19/97.						
	167	Internet pages entitled: Asante and 100BASE-T Fast Ethernet. 7 pages, printed 5/27/97.						
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Page 11 of 14

/M.D./	169	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
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	172	Internet pages directed to Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com, 14 pages, printed 3/15/97.
	173	VIA Technologies, Inc. article entitled "VT8501 Apollo MVP4", pages i-iv, 1-11, cover and copyright page, revision 1.3, Feb. 1, 2000.
	174	iReady News Archives article entitled "iReady Rounding Out Management Team with Two Key Executives", http://www.ireadyco.com/archives/keyexec.html , 2 pages, printed 11/28/98.
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	179	iReady Corporation, article entitled "The I-1000 Internet Tuner", 2 pages, date unknown.
	180	iReady article entitled "About Us Introduction", Internet pages from http://www.iReadyco.com/about.html , 3 pages, printed 11/25/98.
	181	iReady News Archive article entitled "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", San Jose, CA, November 20, 1997. 2 pages, printed 11/2/98.
↓	182	iReady News Archive article entitled "Seiko Instruments Inc. (SII) INTRODUCES WORLD'S FIRST INTERNET-READY INTELLIGENT LCD MODULES BASED ON IREADY TECHNOLOGY," Santa Clara, CA and Chiba, Japan, October 26, 1998. 2 pages, printed 11/2/98.

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Page 12 of 14

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	193	"File System Design For An NFS File Server Appliance", Article by D. Hitz, et al., 13 pages.
	194	Adaptec Press Release article entitled "Adaptec Announces EtherStorage Technology", 2 pages, May 4, 2000, printed 6/14/00.
	195	Adaptec article entitled "EtherStorage Frequently Asked Questions", 5 pages, printed 7/19/00.
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Page 13 of 14

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	208	Andrew S. Tanenbaum, Computer Networks, Third Edition, 1996, ISBN 0-13-349945-6.
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Page 14 of 14

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